



Software Tracing of Virtual Hardware using **Emul8 and Tracealyzer**

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Virtual hardware?

- Use a system simulator as development platform
 - Instruction-level accurate and surprisingly fast
- Emul8
 - System simulator for embedded software development
 - Developed by Antmicro and Real-Time Embedded



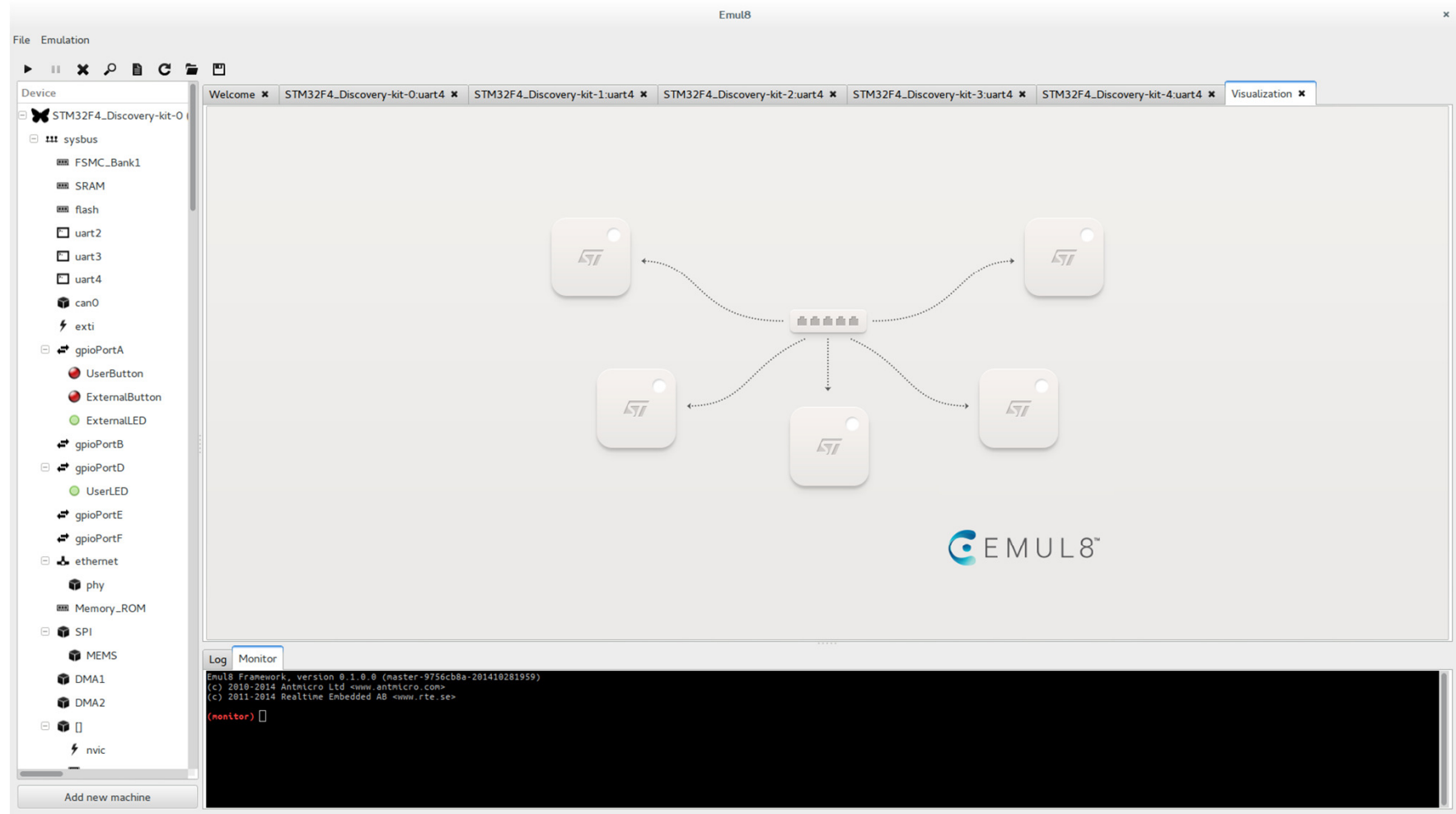
Why use virtual hardware platforms?

- Start software development earlier
- Developers not limited by shared hardware
- Simulate full system with multiple processors
- Full control
 - Everything is accessible and deterministic
 - Save system state and rewind/resume debugging
 - Deterministic automated testing on system level

Issues

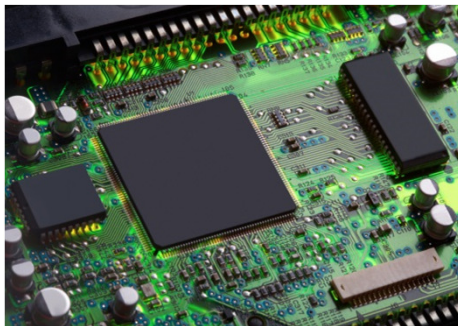
- Hardware models
 - Modular, parts can be reused
 - Model libraries available
- Accuracy
 - Approximate timing, not cycle-accurate
 - Deterministic – no “random variations”
 - Insert timing variations to increase coverage?

Emul8



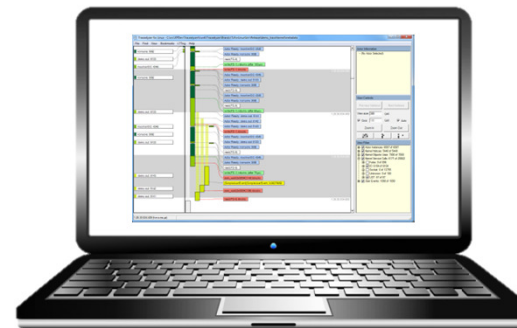
Tracing shows the run-time behavior

Record

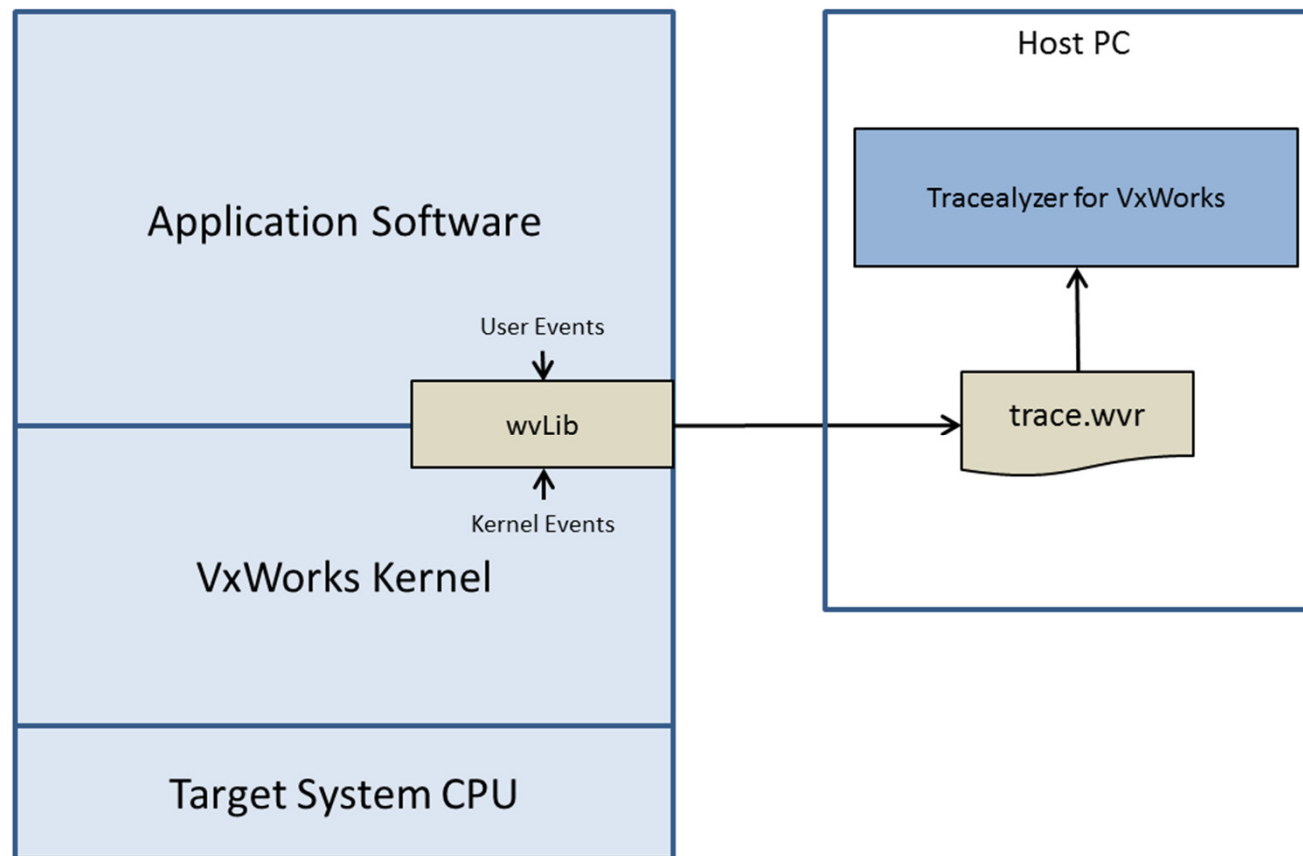


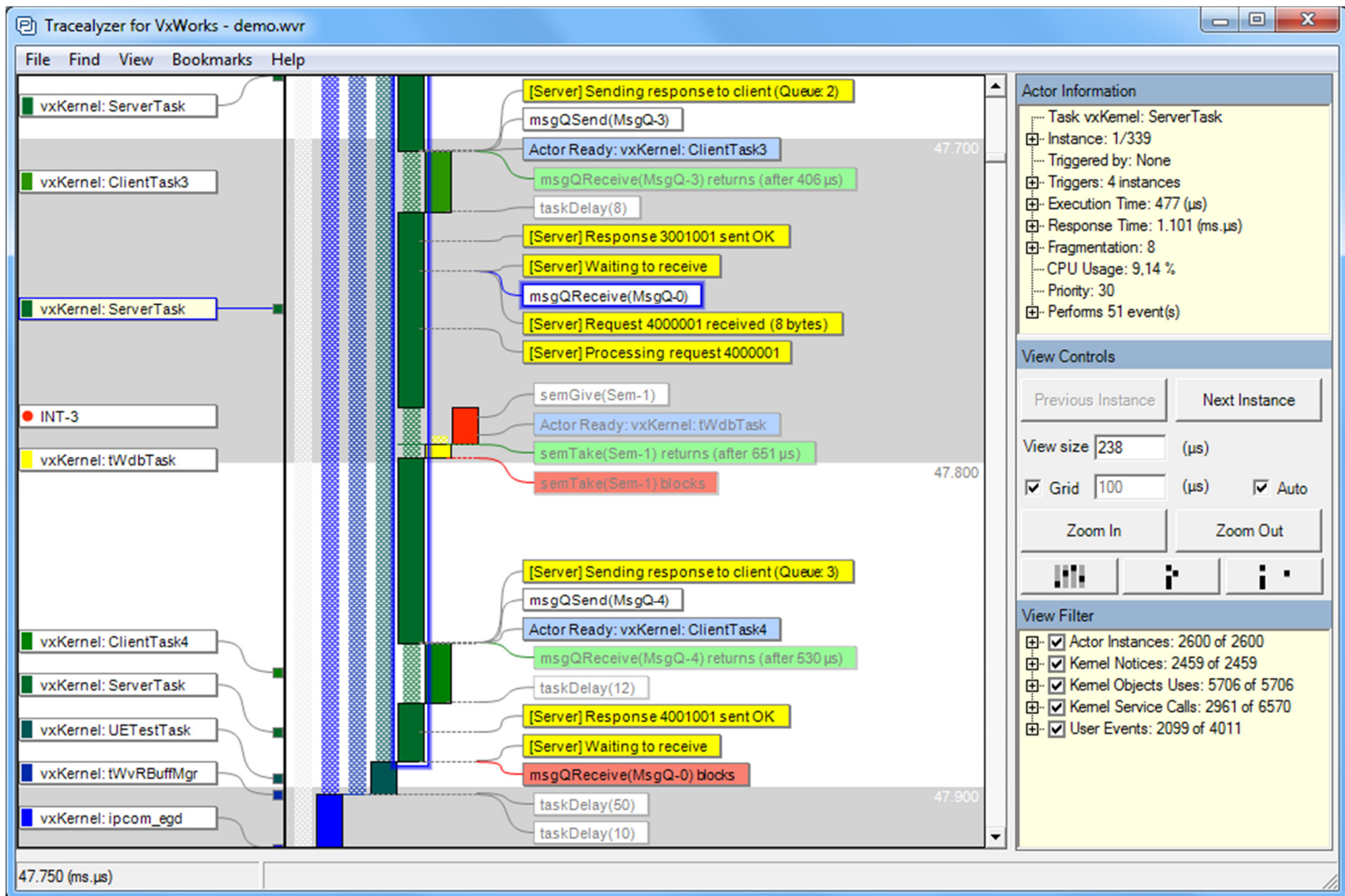
Software Behavior

Visualize



Software Trace





MsgQ-0 (MsgQ)

Timestamp	Actor	Event	Block time	Status	Size	Queue
47.242	vxKernel: ClientT	msgQSend		Sent post #1	1	1
47.269	vxKernel: ClientT	msgQSend		Sent post #2	2	1 2
47.297	vxKernel: ClientT	msgQSend		Sent post #3	3	1 2 3
47.325	vxKernel: ClientT	msgQSend		Sent post #4	4	1 2 3 4
47.325	vxKernel: ServerT	msgQReceive		Received post #1	3	1 2 3 4
47.453	vxKernel: ServerT	msgQReceive		Received post #2	2	2 3 4
47.601	vxKernel: ServerT	msgQReceive		Received post #3	1	3 4
47.740	vxKernel: ServerT	msgQReceive		Received post #4	0	4
47.892	vxKernel: ServerT	msgQReceive	2.731	Trying to receive...	0	Empty
50.622	vxKernel: ClientT	msgQSend		Sent post #5	1	5
50.622	vxKernel: ServerT	msgQReceive		Received post #5	0	5
50.759	vxKernel: ServerT	msgQReceive	867	Trying to receive...	0	Empty
51.625	vxKernel: ClientT	msgQSend		Sent post #6	1	6
51.626	vxKernel: ServerT	msgQReceive		Received post #6	0	6
51.766	vxKernel: ServerT	msgQReceive	2.856	Trying to receive...	0	Empty
54.622	vxKernel: ClientT	msgQSend		Sent post #7	1	7
54.622	vxKernel: ServerT	msgQReceive		Received post #7	0	7
54.752	vxKernel: ServerT	msgQReceive	919	Trying to receive...	0	Empty
55.670	vxKernel: ClientT	msgQSend		Sent post #8	1	8
55.670	vxKernel: ServerT	msgQReceive		Received post #8	0	8
55.803	vxKernel: ServerT	msgQReceive	824	Trying to receive...	0	Empty
56.626	vxKernel: ClientT	msgQSend		Sent post #9	1	9
56.627	vxKernel: ServerT	msgQReceive		Received post #9	0	9
56.747	vxKernel: ServerT	msgQReceive	1.752	Trying to receive...	0	Empty
58.499	vxKernel: ClientT	msgQSend		Sent post #10	1	10
58.499	vxKernel: ServerT	msgQReceive		Received post #10	0	10
58.696	vxKernel: ServerT	msgQReceive	735	Trying to receive...	0	Empty
59.430	vxKernel: ClientT	msgQSend		Sent post #11	1	11
59.431	vxKernel: ServerT	msgQReceive		Received post #11	0	11

Timestamp: 47.325

Actor: vxKernel: ClientTask4

Event: msgQSend

Status: Instant

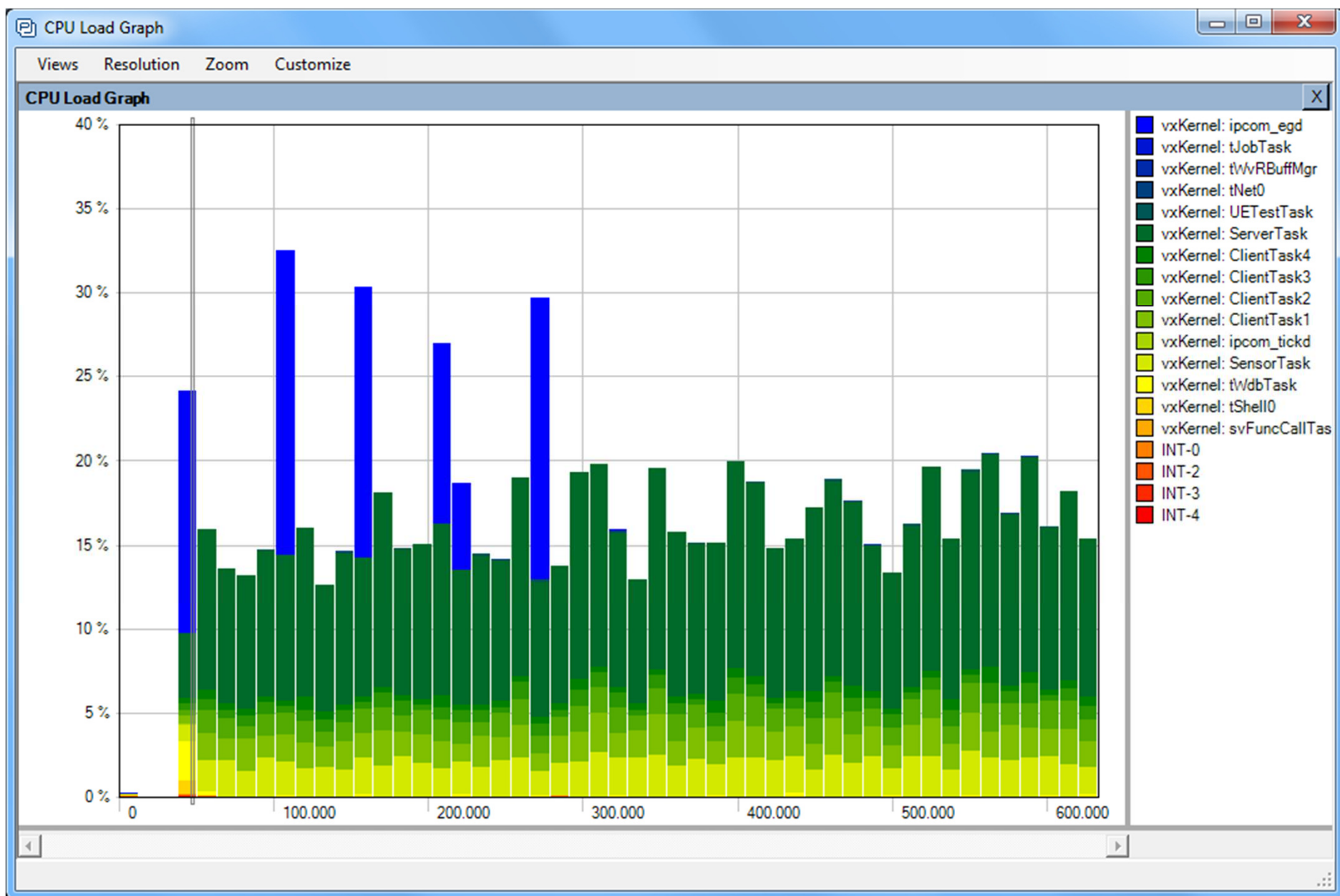
Parameter: N/A

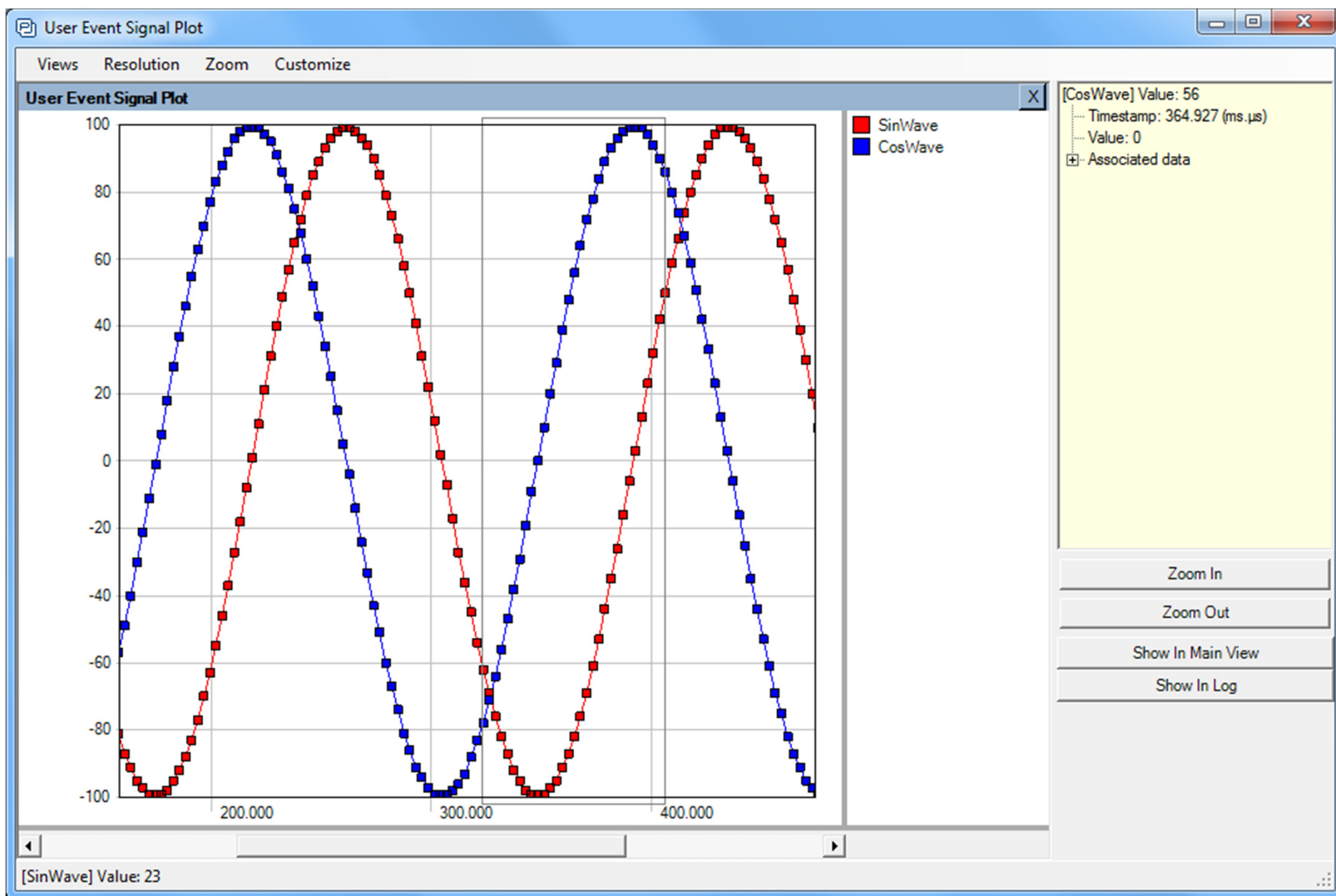
Goto Entry/Exit Event

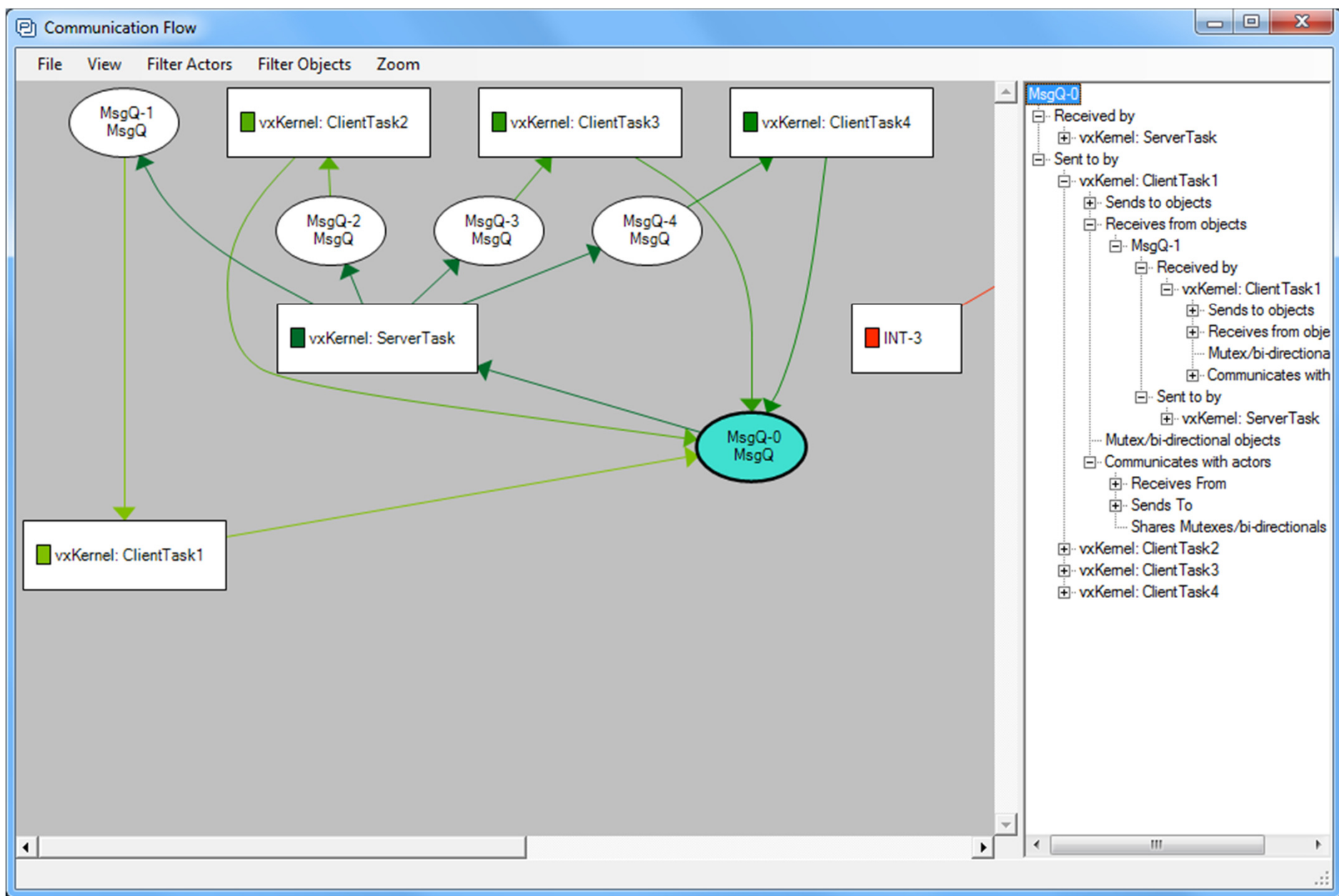
Show in Trace

Goto Sending Event

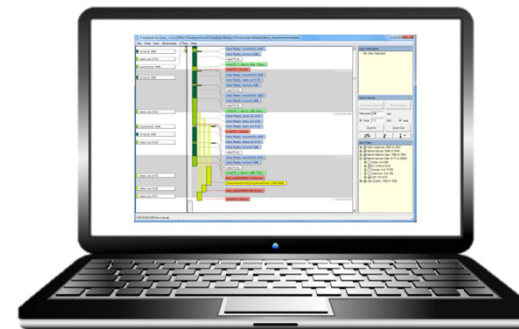
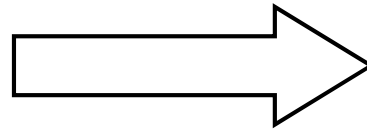
Goto Receiving Event







Tracing on virtual hardware platforms



System simulator - extended with OS-aware tracing!

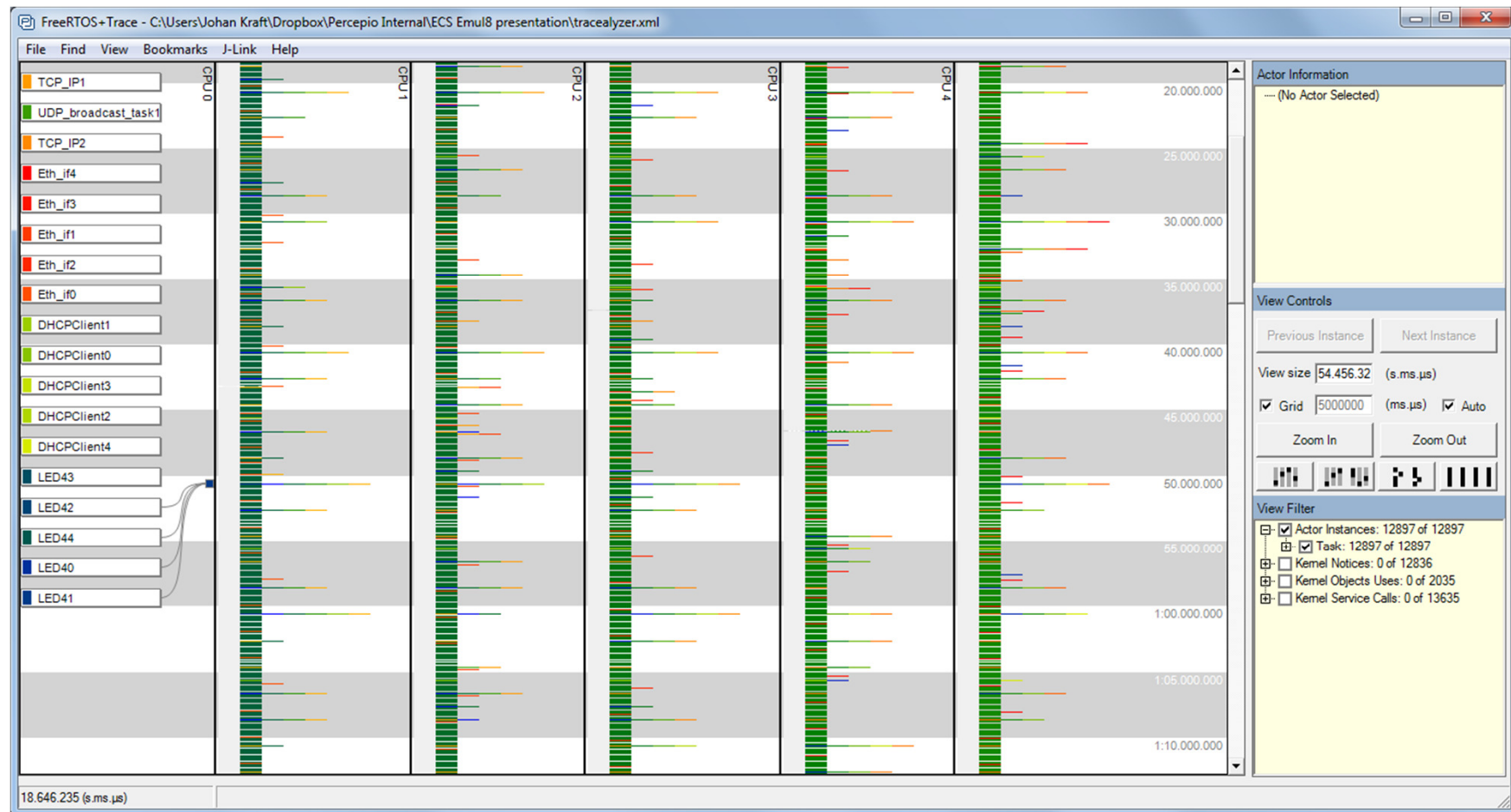
Comparison

	Hardware Trace	Software Trace	Simulator Trace
Max. level of detail	Instruction-level	Selected software events	Instruction-level
Impact/Overhead	None	CPU and RAM usage	None
What can be recorded	Hardware dependent (control and/or data)	Any software event	Any simulator event (software or hardware)
Hardware requirements	Trace-enabled CPU, board and debugger	None	None

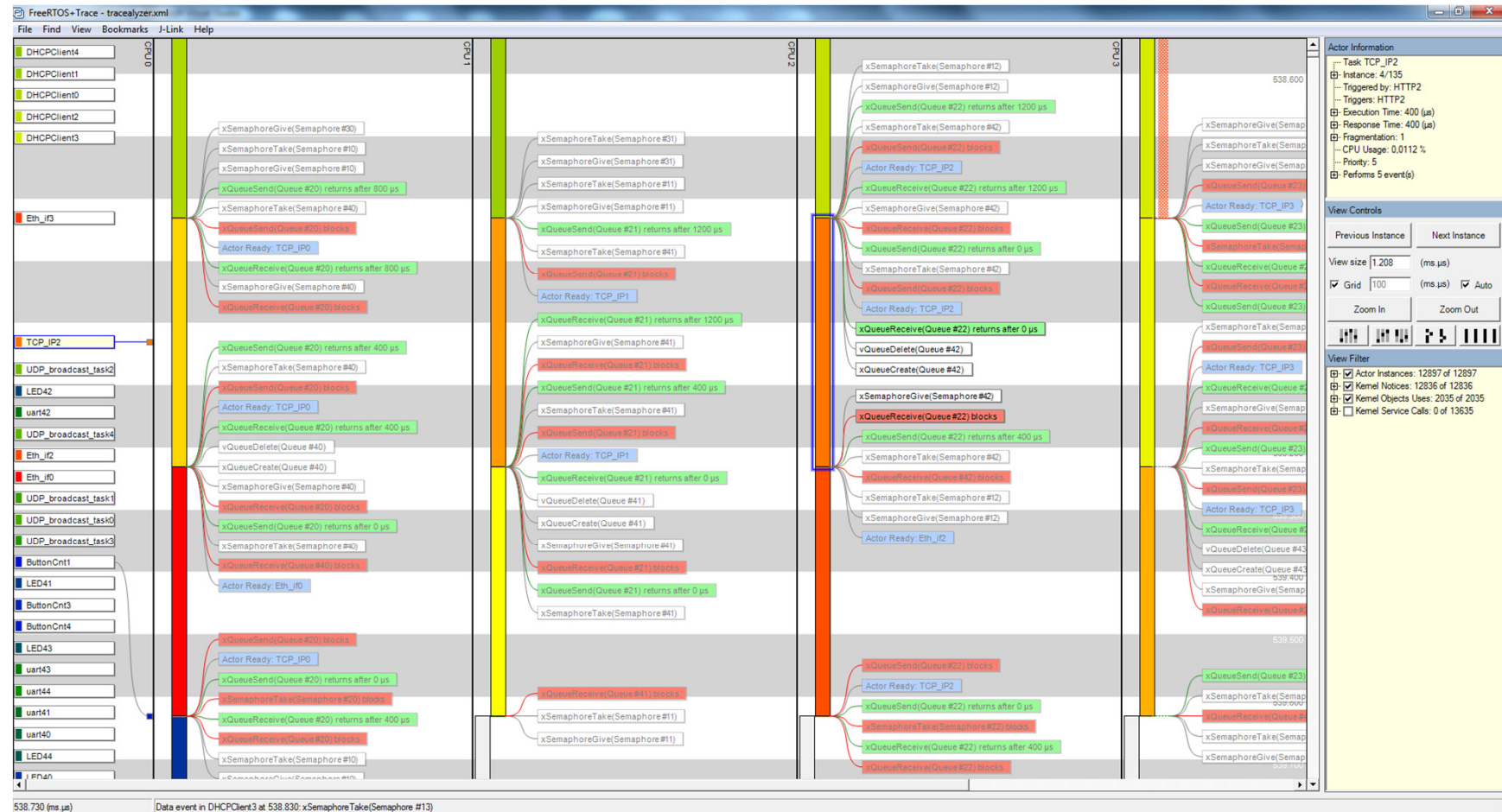
Prototype implementation

- Emul8 extended with
 - FreeRTOS awareness
 - Tracealyzer trace output (OS events)
- Demonstration system
 - 5 x STM32F4xx microcontrollers
 - FreeRTOS and TCP/IP communication
 - Physically implemented by RTE and Antmicro

Emul8 in Tracealyzer - Zoomed out



Emul8 in Tracealyzer - Zoomed in



Emul8 in Tracealyzer - Other views...



Thanks for listening!

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